

# Capacitive Load 763366 (8331-00)

**FESTO**

LabVolt Series

Datasheet



## Table of Contents

General Description	2
Specifications	2

## General Description

The Capacitive Load consists of a module housing nine capacitors arranged in three identical banks. Each bank consists of three capacitors connected in parallel that can be switched on or off with toggle switches to obtain various capacitance values. This allows the equivalent capacitance of each bank to be increased or decreased by steps. Six safety banana jacks on the module front panel provide access to each capacitor bank. The three capacitor banks can be connected separately for operation in three-phase circuits. Also, the three capacitor banks can be connected together for operation in single-phase circuits.

A permanently connected discharge resistor reduces the voltage across the terminals of each bank of capacitors to 5% of the applied voltage within 25 seconds after the load is disconnected from the supply. The Capacitive Load may be used with both dc and ac power.

The Capacitive Load is commonly used in conjunction with the other basic load modules, the Resistive Load and the Inductive Load to experiment with the effects of different types of load on a circuit.

## Specifications

Parameter	Value
<b>Capacitors</b>	
Quantity	Three identical banks of three capacitors
Capacitance Values (Each Bank)	2.2/4.4/8.8 $\mu\text{F}$
Reactance Values (Each Bank)	300/600/1200 $\Omega$
Nominal Voltage	120 V – 60 Hz
Maximum Voltage	230 V
Capacitance Value Accuracy	$\pm 5\%$
<b>Load at Nominal Voltage (Each Bank)</b>	
Reactive Power	12-84 var
Current	0.1-0.7 A
Steps	Seven, of equal increment
Current Increment	0.1 A
<b>Physical Characteristics</b>	
Dimensions (H x W x D)	154 x 287 x 410 mm (6.1 x 11.3 x 16.1 in)
Net Weight	5.7 kg (12.6 lb)

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